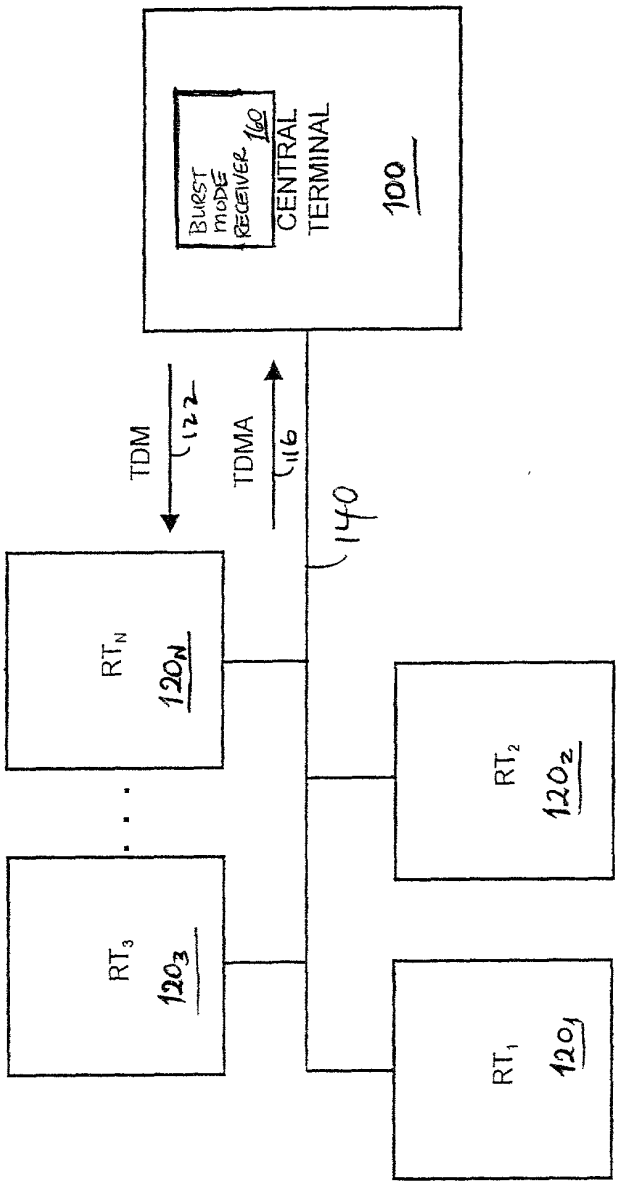

102
BURST MODE RECEIVER 160
CENTRAL TERMINAL
100
TDM
122
TDMA
116
140
RT$_N$
120$_N$
RT$_3$
120$_3$
RT$_2$
120$_2$
RT$_1$
120$_1$

FIG. 1

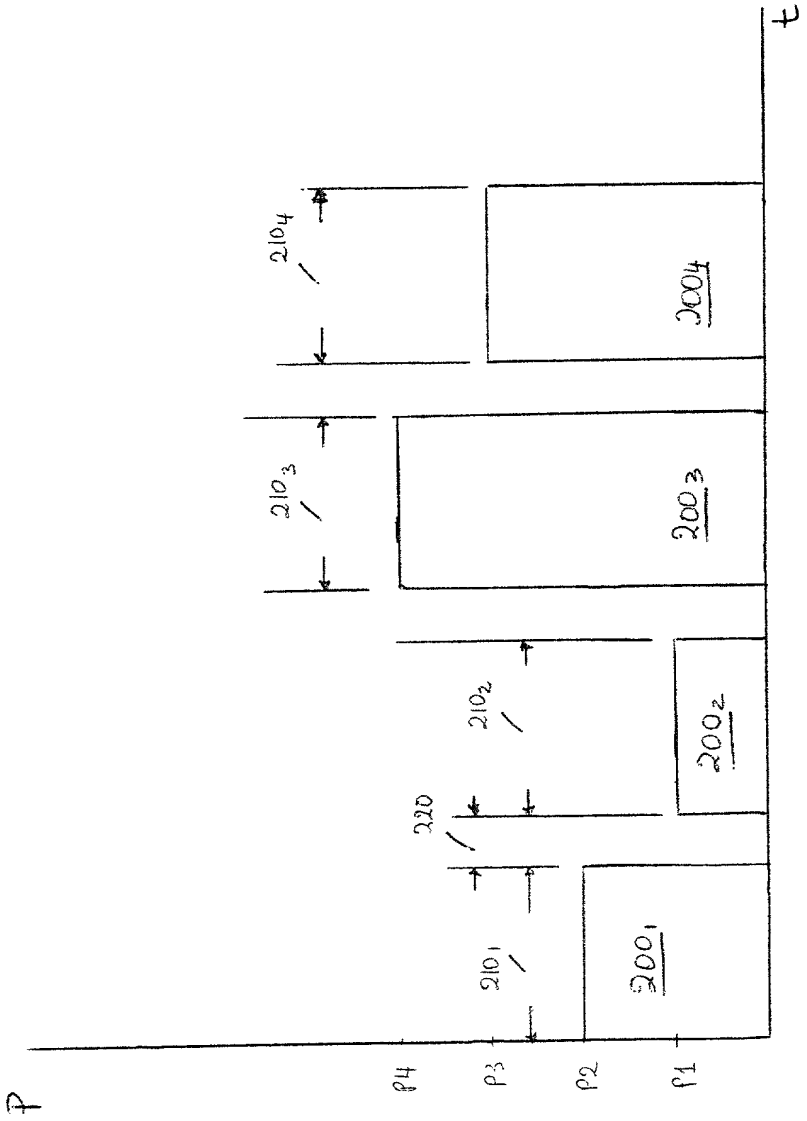


FIG. 2A

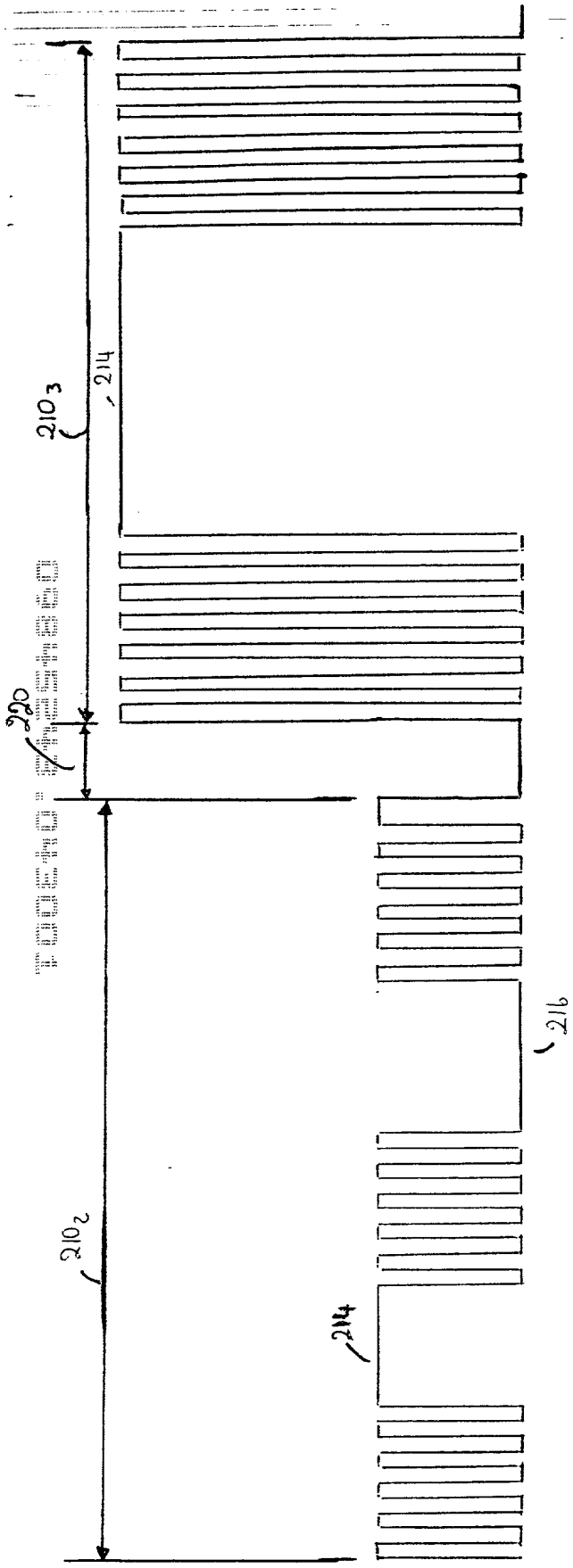


FIG. 2B

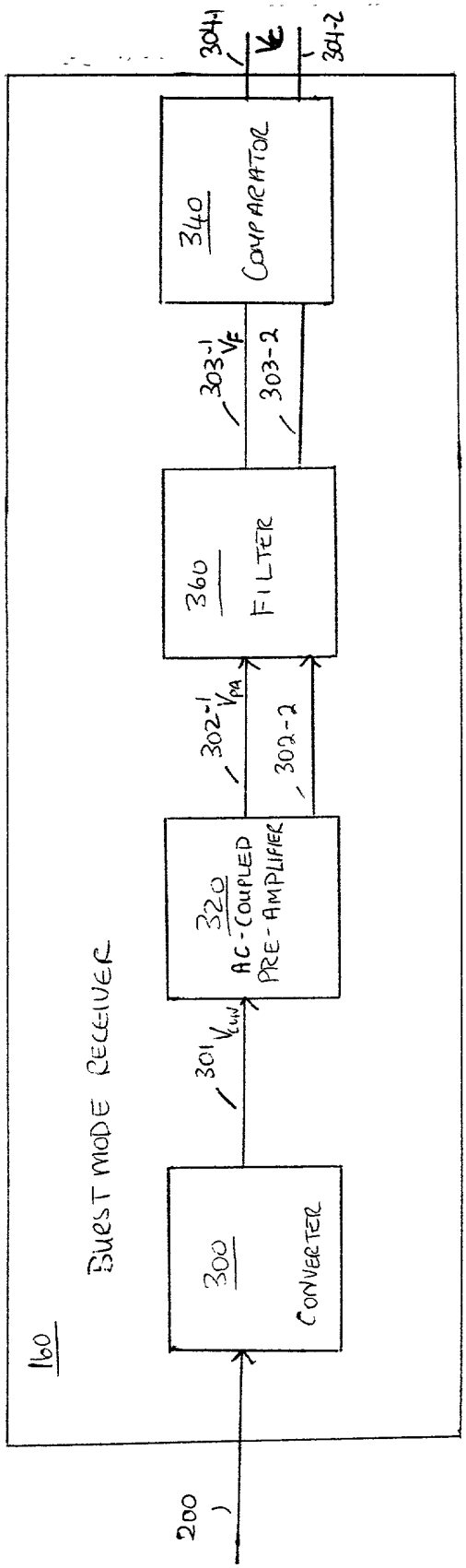

200
160
BURST MODE RECEIVER
300
CONVERTER
301
V_{LIM}
320
AC-COUPLED
PRE-AMPLIFIER
302-1
V_{PA}
302-2
360
FILTER
303-1
V_F
303-2
340
COMPARATOR
304-1
V_C
304-2

FIG. 3

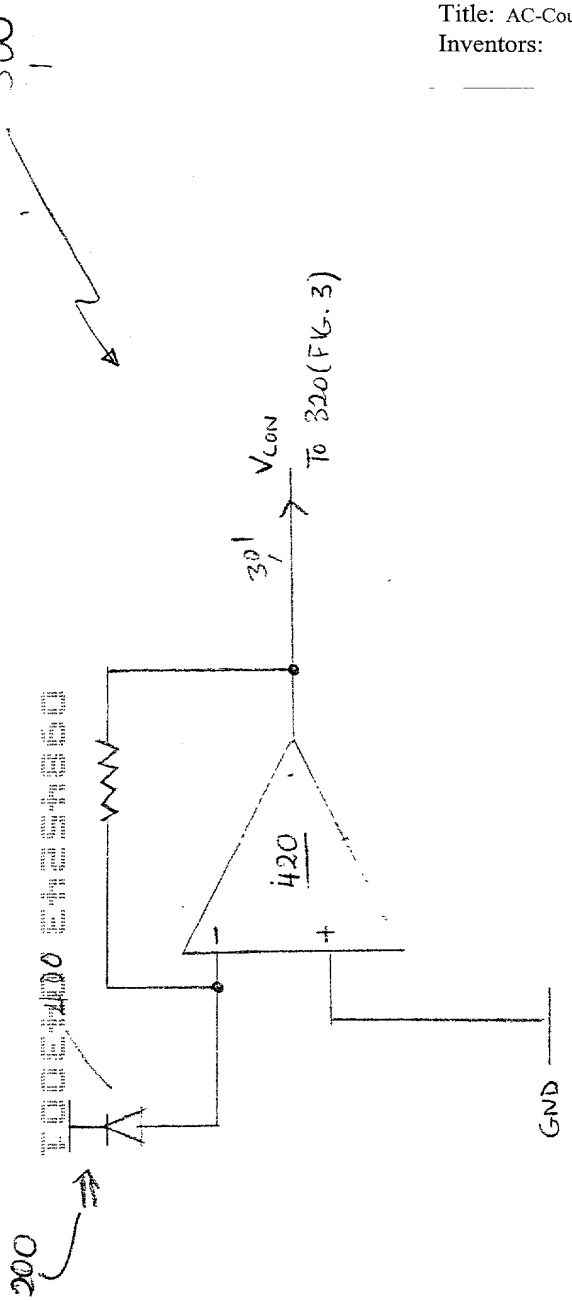


FIG. 4

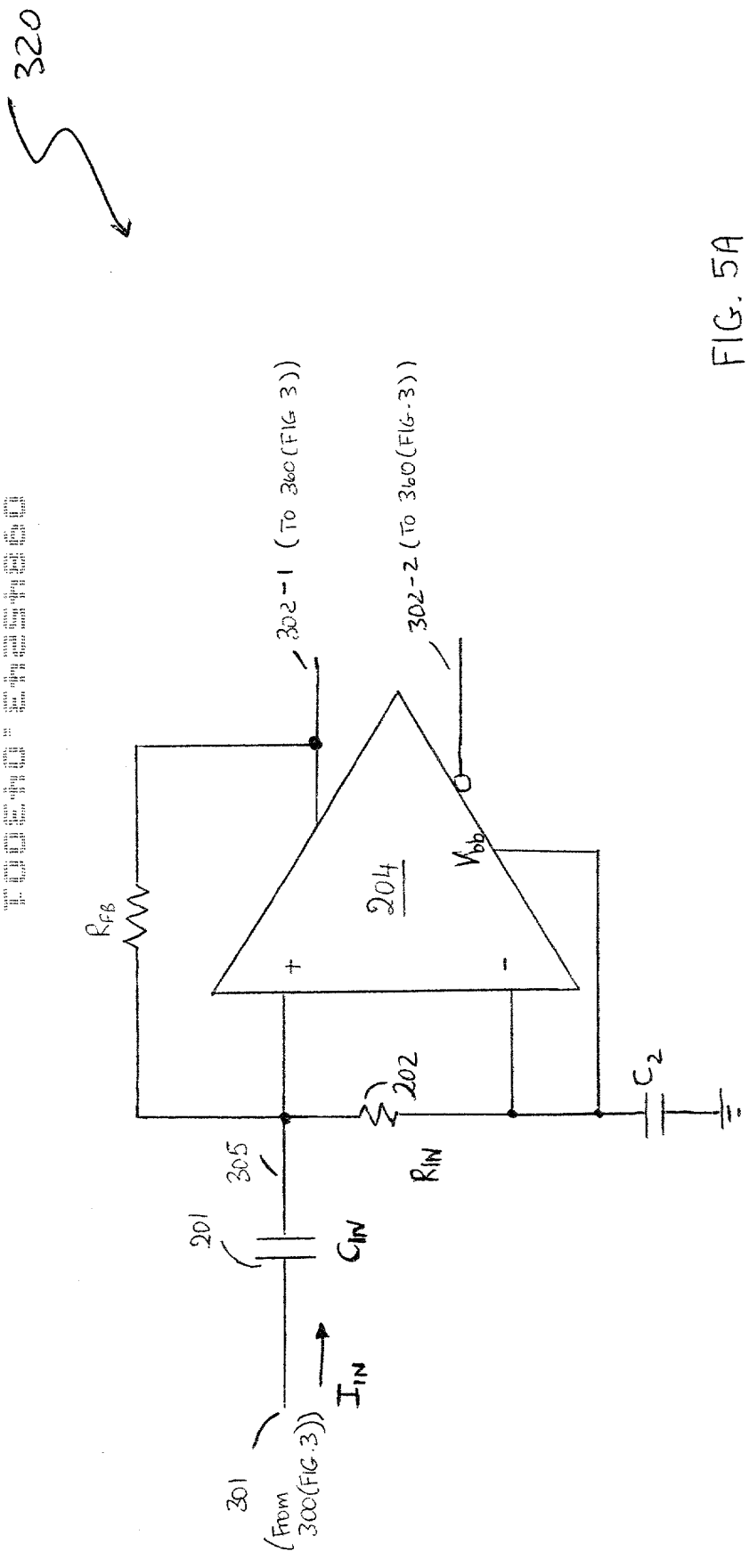

320
302-1 (TO 360 (FIG 3))
302-2 (TO 360 (FIG. 3))
204
V_{bb}
R_{FB}
+
-
202
C_2
R_{IN}
305
201
C_{IN}
I_{IN}
301
(FROM 300 (FIG. 3))

FIG. 5A

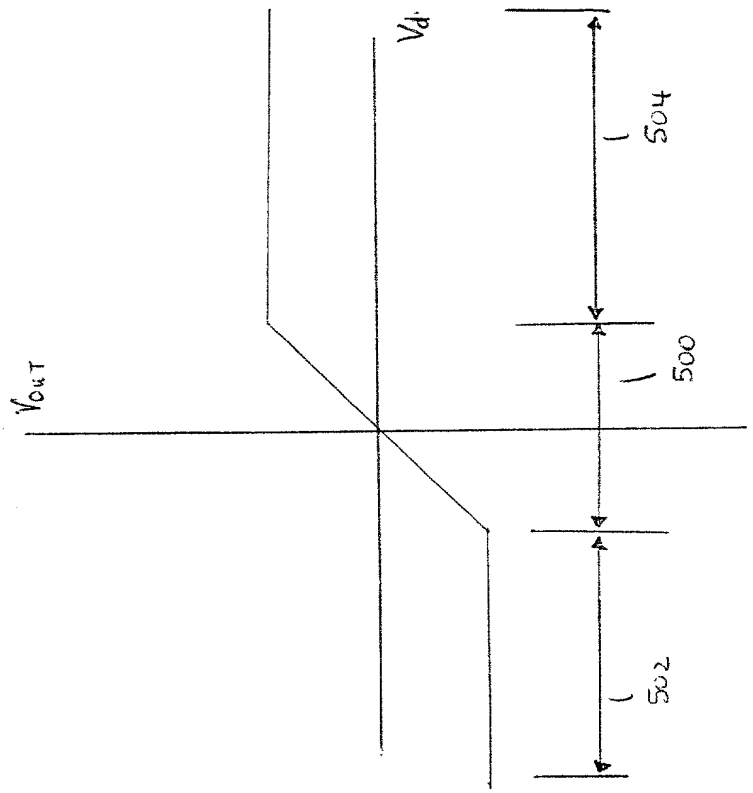


FIG. 5B

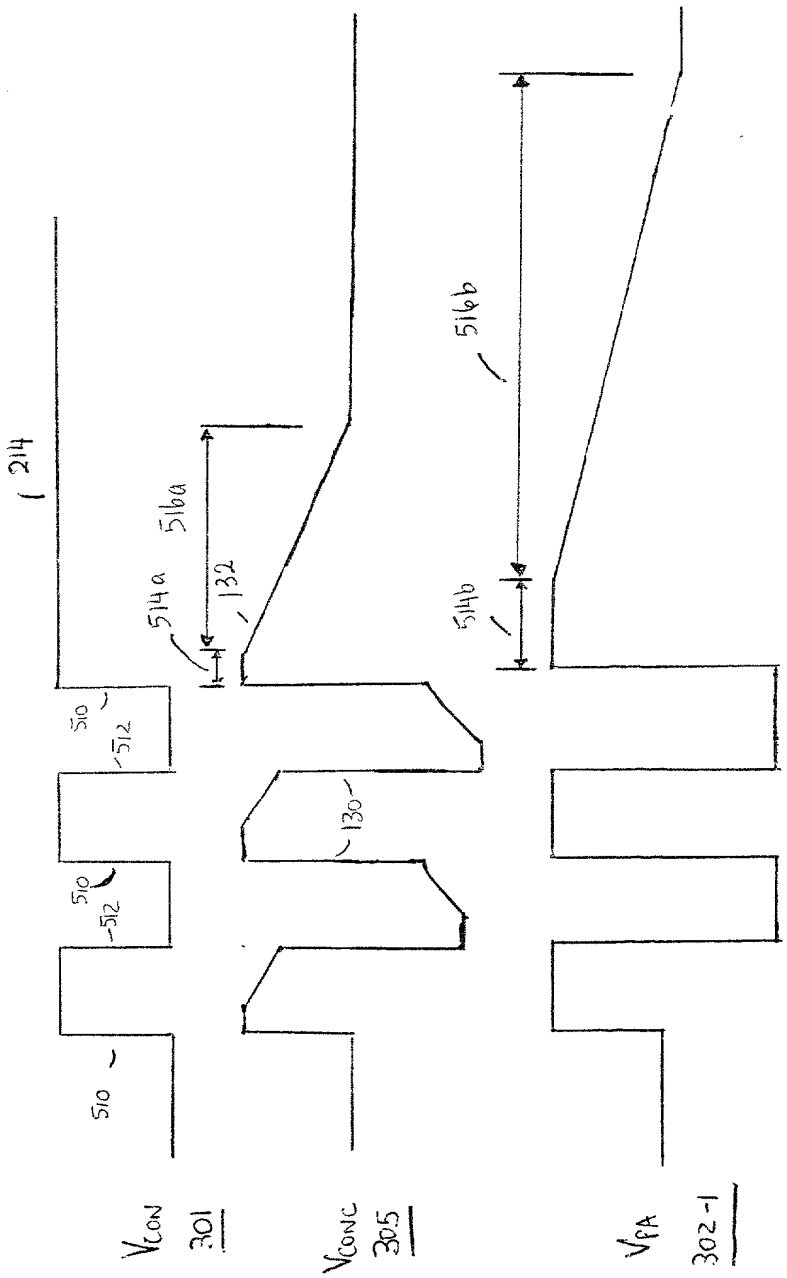

214
516a
514a
132
516b
514b
510
512
510
512
510
130
V_{CON} 301
V_{CONC} 305
V_{PA} 302-1

FIG. 5C

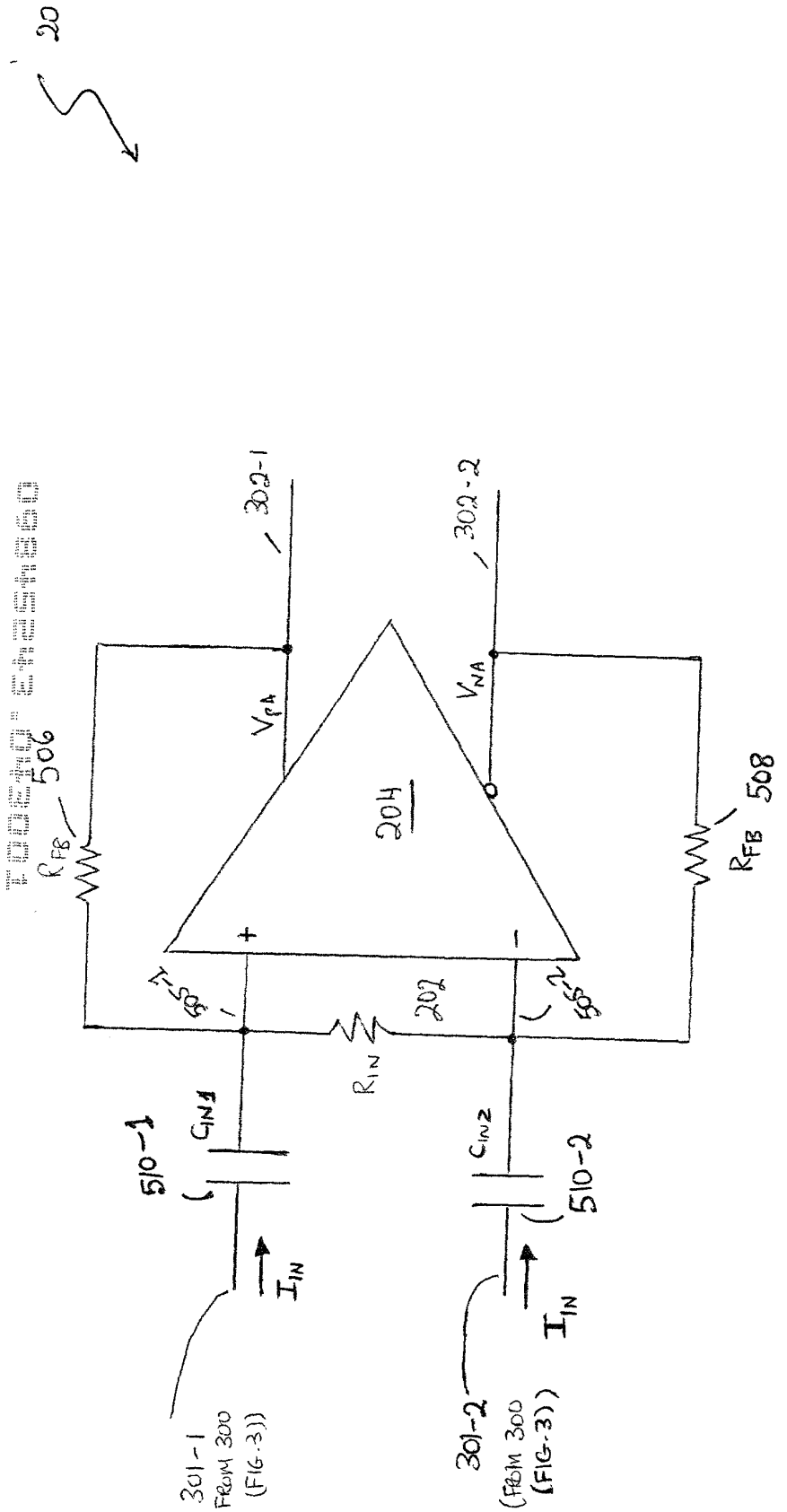

20
302-1
302-2
V_{FA}
V_{NA}
204
506
R_{FB}
508
R_{FB}
+
-
206-1
206-2
202
R_{IN}
C_{IN1}
C_{IN2}
510-1
510-2
I_{IN}
I_{IN}
301-1
(FROM 300
(FIG-3))
301-2
(FROM 300
(FIG-3))

FIG. 5D

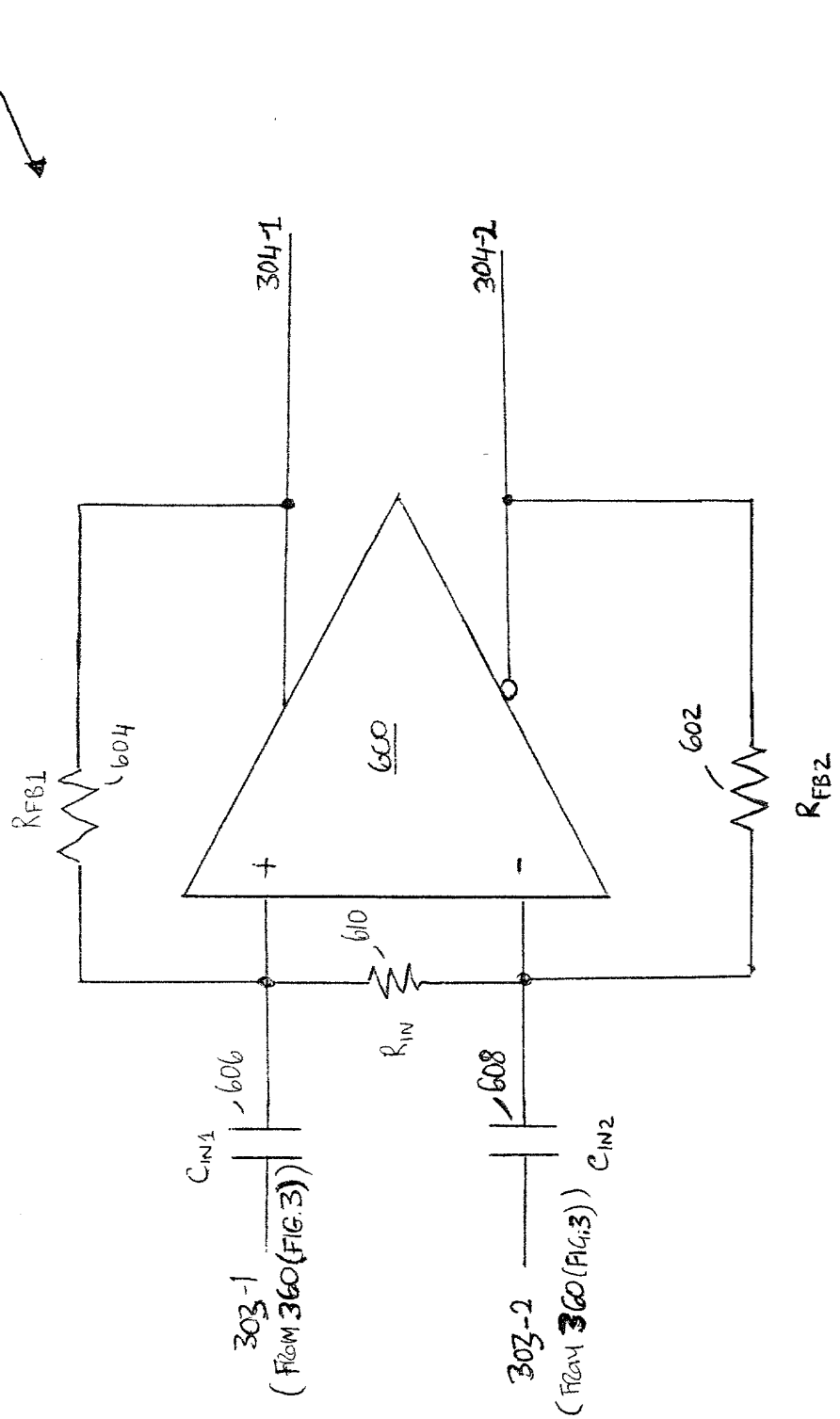
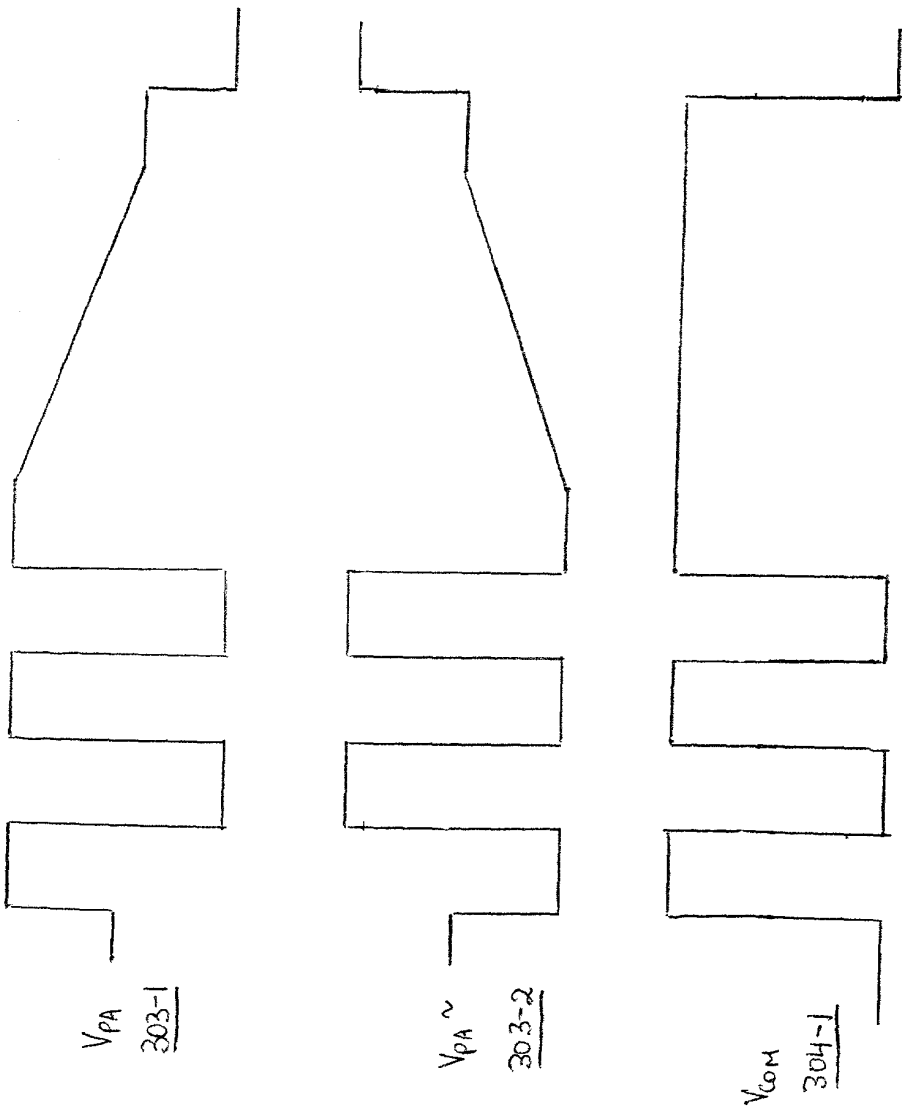

340
304-1
304-2
600
+
-
602
R_{FB2}
604
R_{FB1}
610
R_{IN}
606
C_{IN1}
608
C_{IN2}
303-1
(FROM 360 (FIG. 3))
303-2
(FROM 360 (FIG. 3))

FIG. 6A

FIG. 6B



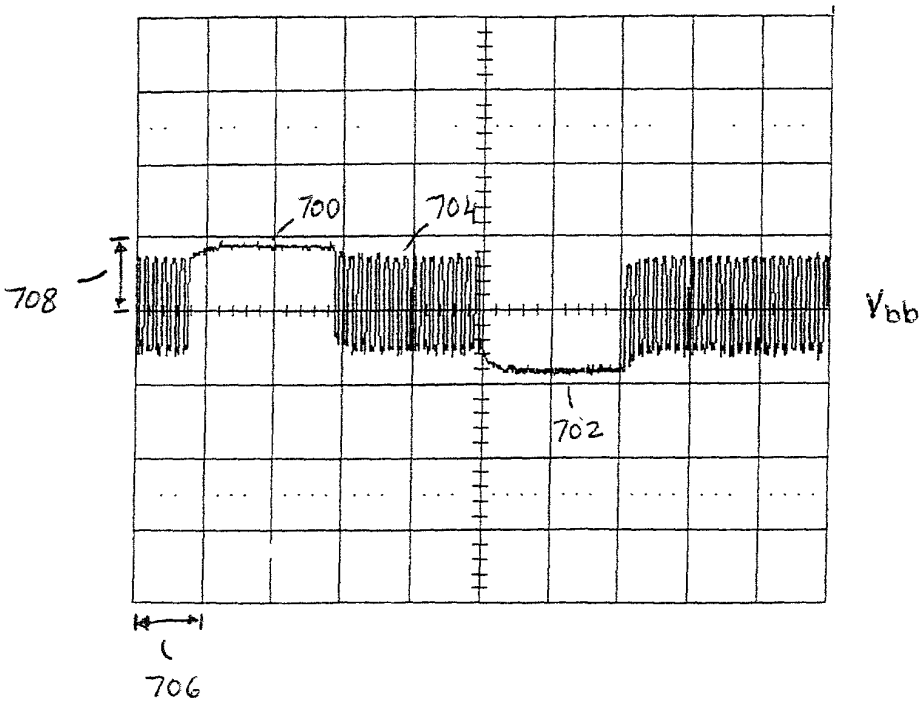


FIG. 7

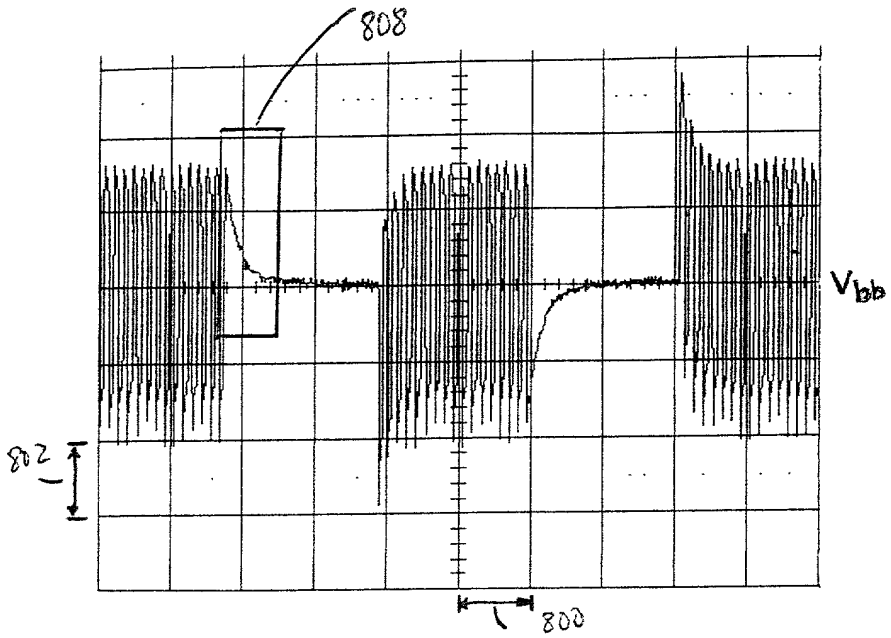


FIG. 8A

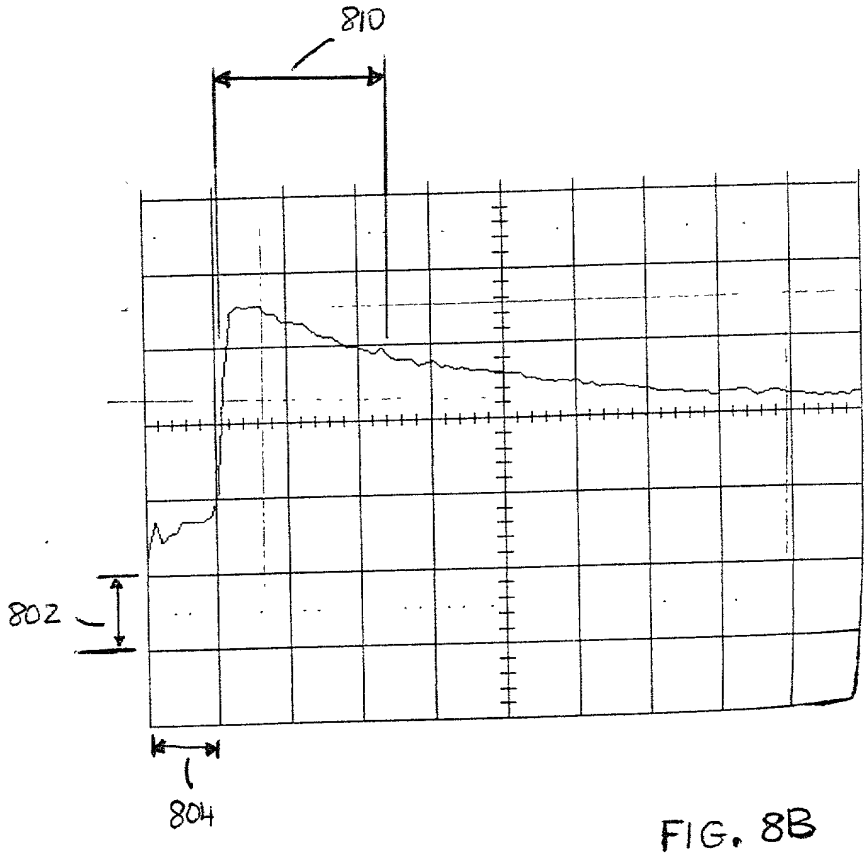


FIG. 8B

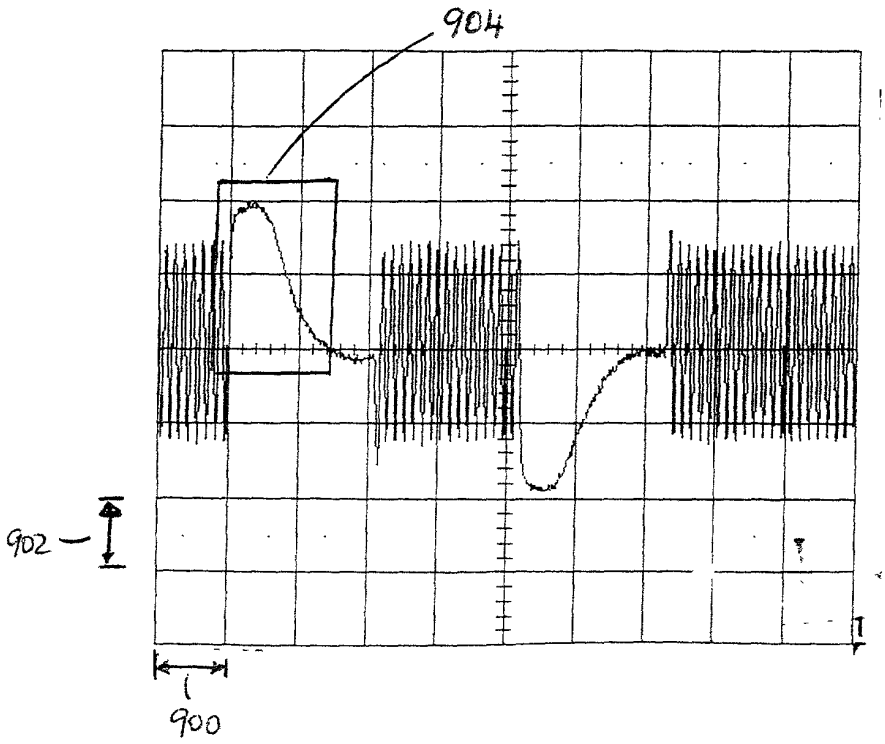


FIG. 9A

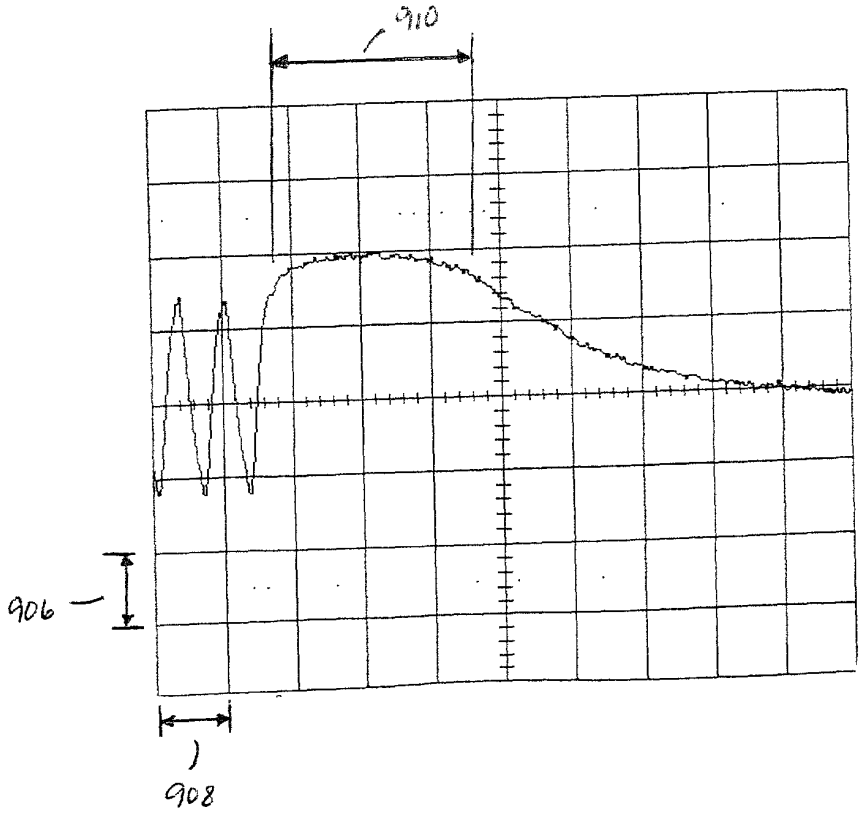


FIG. 9B

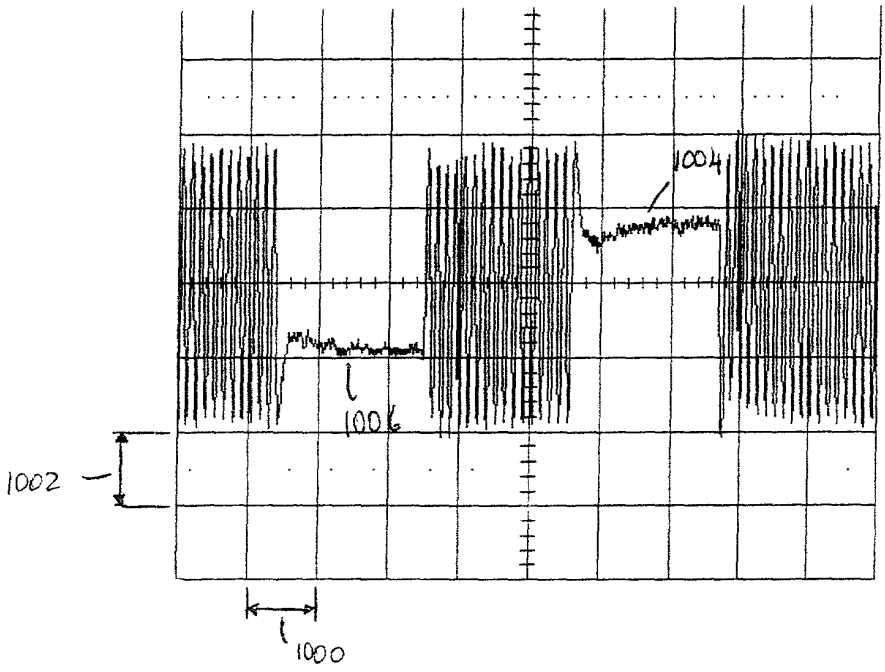


FIG. 10A

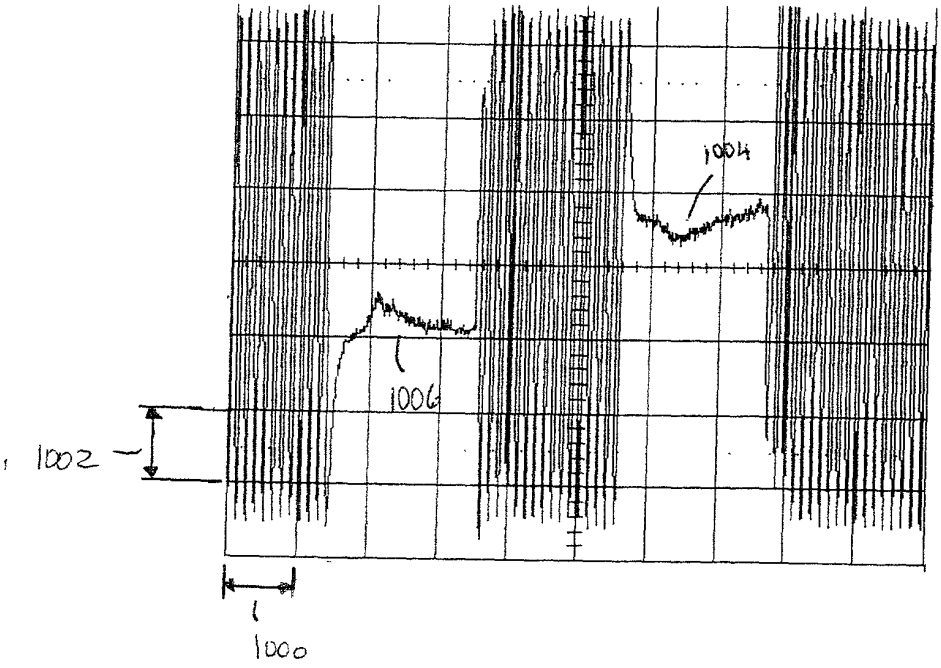


FIG. 10B

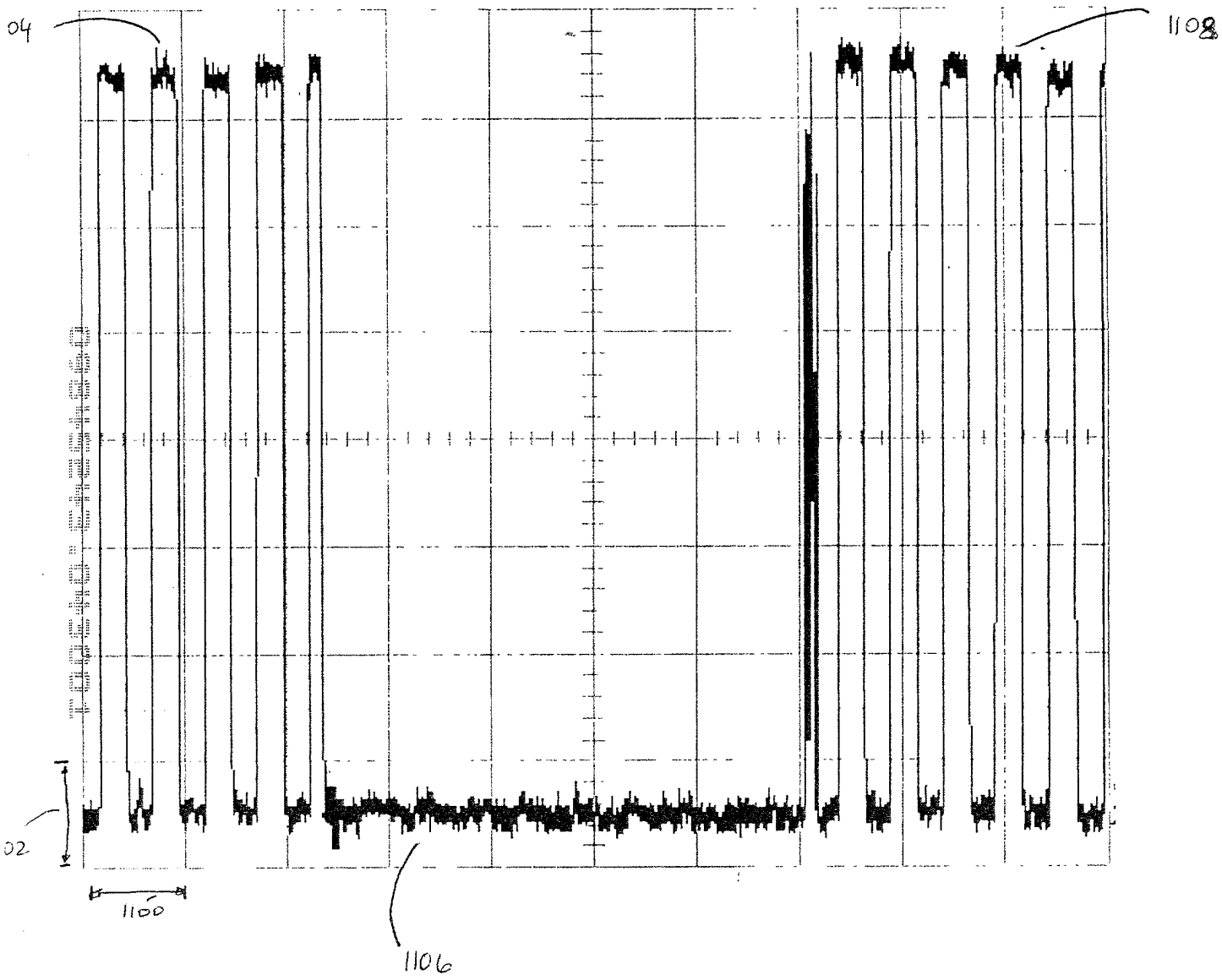


FIG. 11A

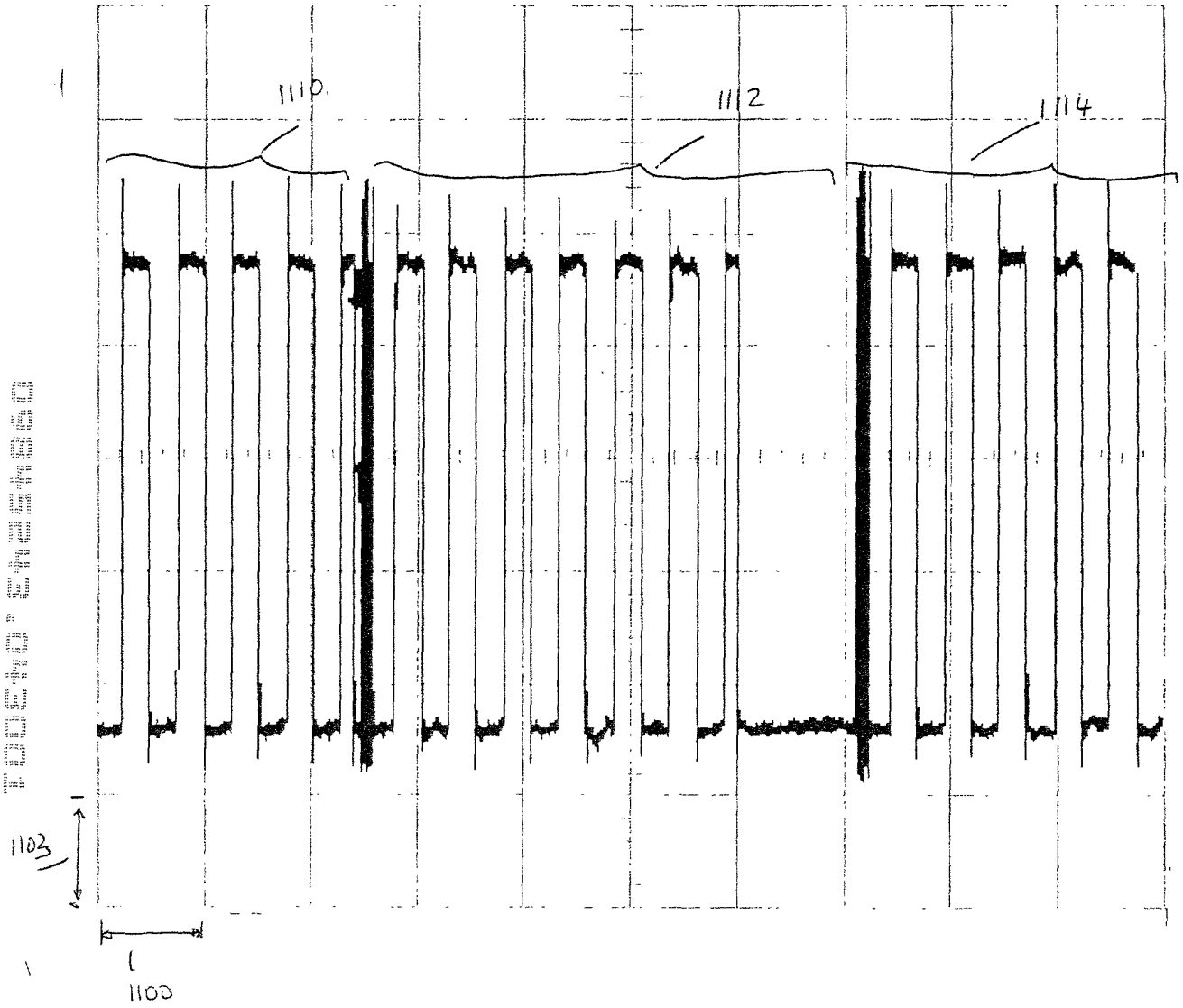


FIG. 11 B

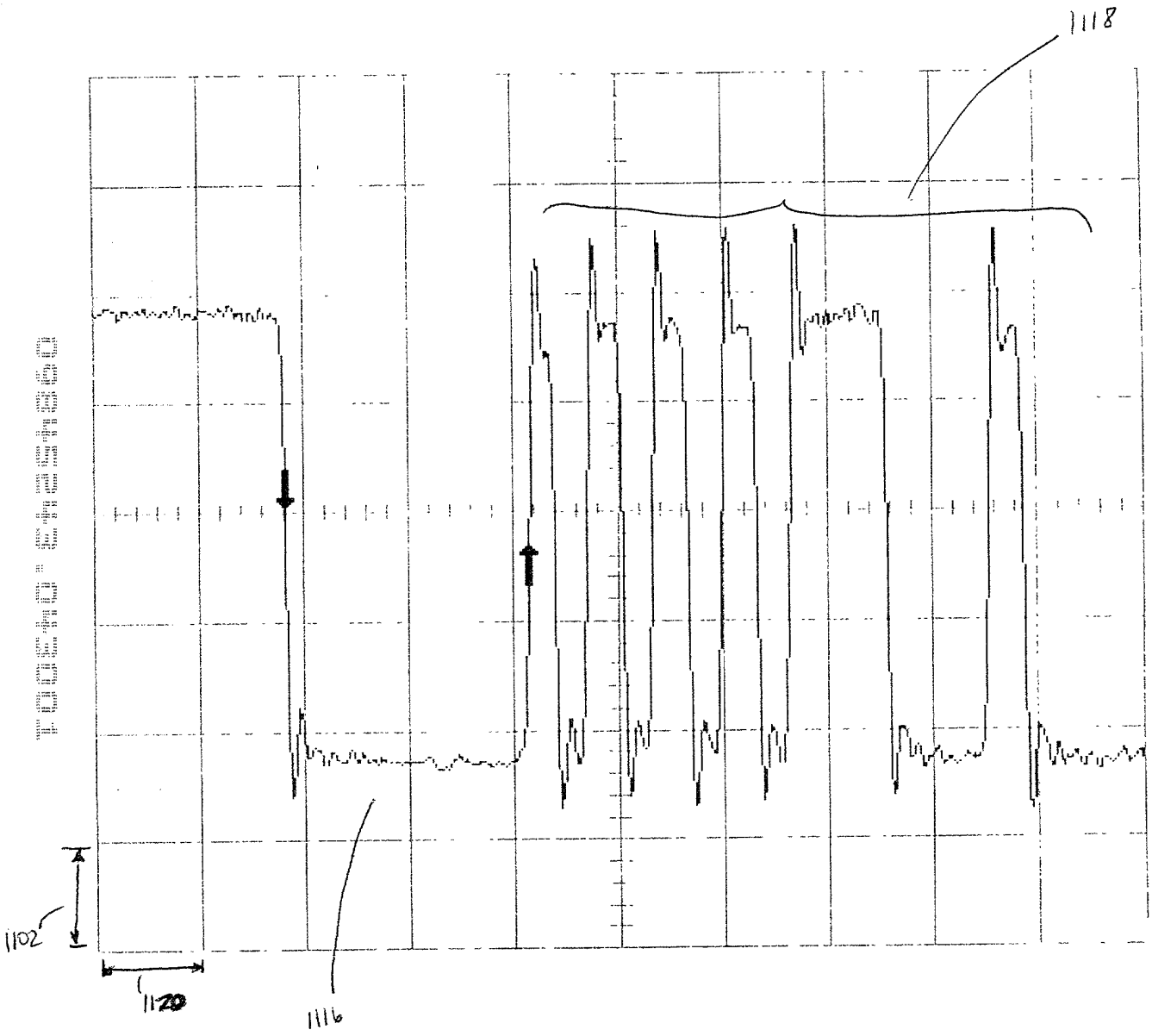


FIG. 11 C